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(58) Field of Search

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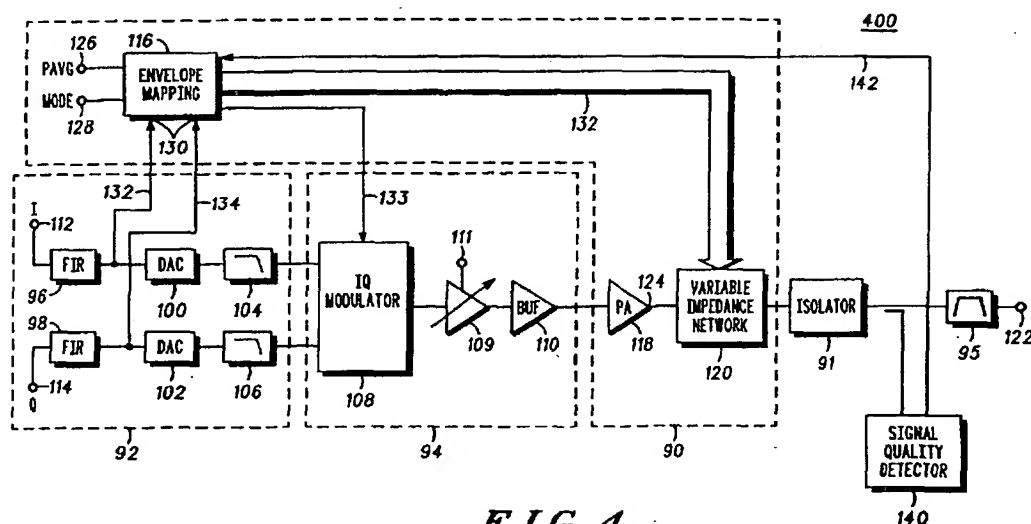
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(54) Abstract Title

Transmitter amplifier arrangement with envelope following impedance network

(57) An envelope following amplifier arrangement for a transmitter is described, in which an impedance network 120 connected to the output of the power amplifier 118 is controlled to follow the envelope and so improve the efficiency. A pre-distortion arrangement is also described.



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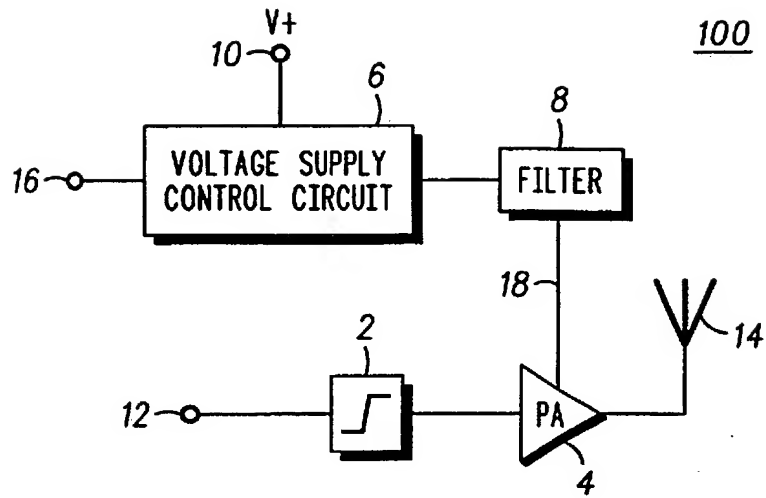


FIG. 1

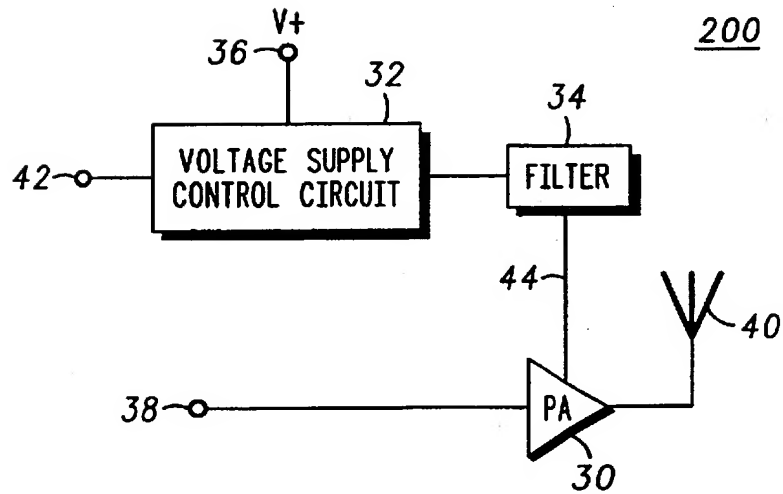
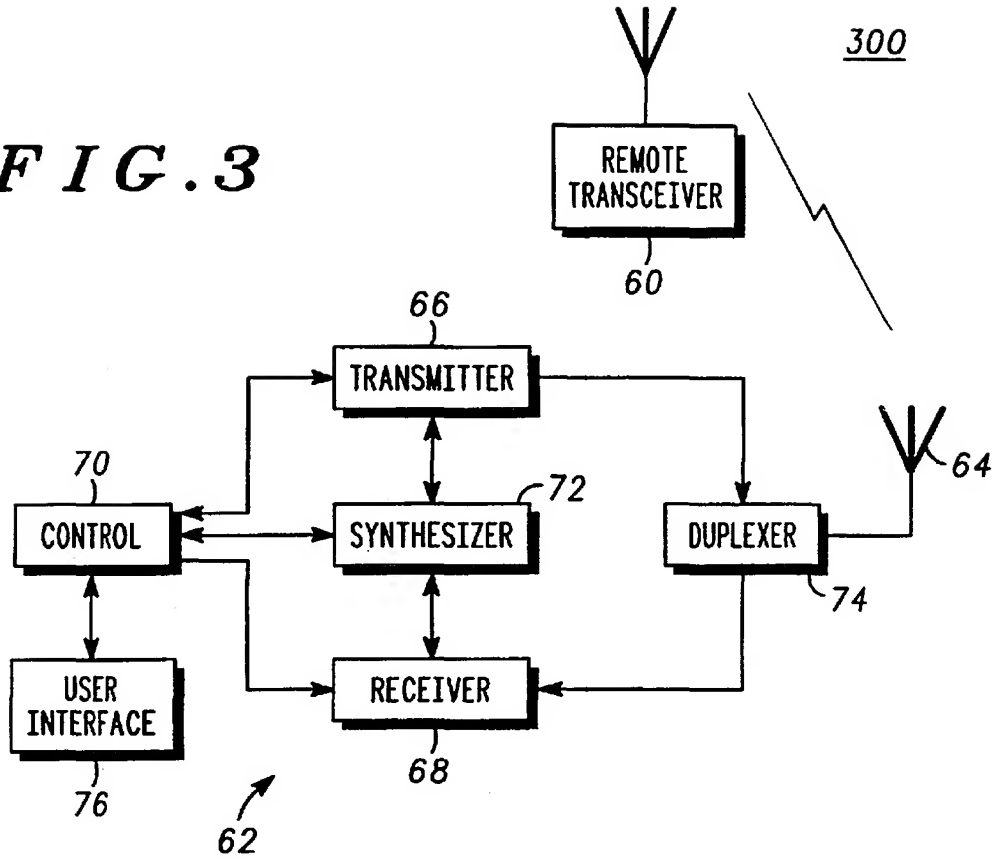


FIG. 2

FIG. 3



700

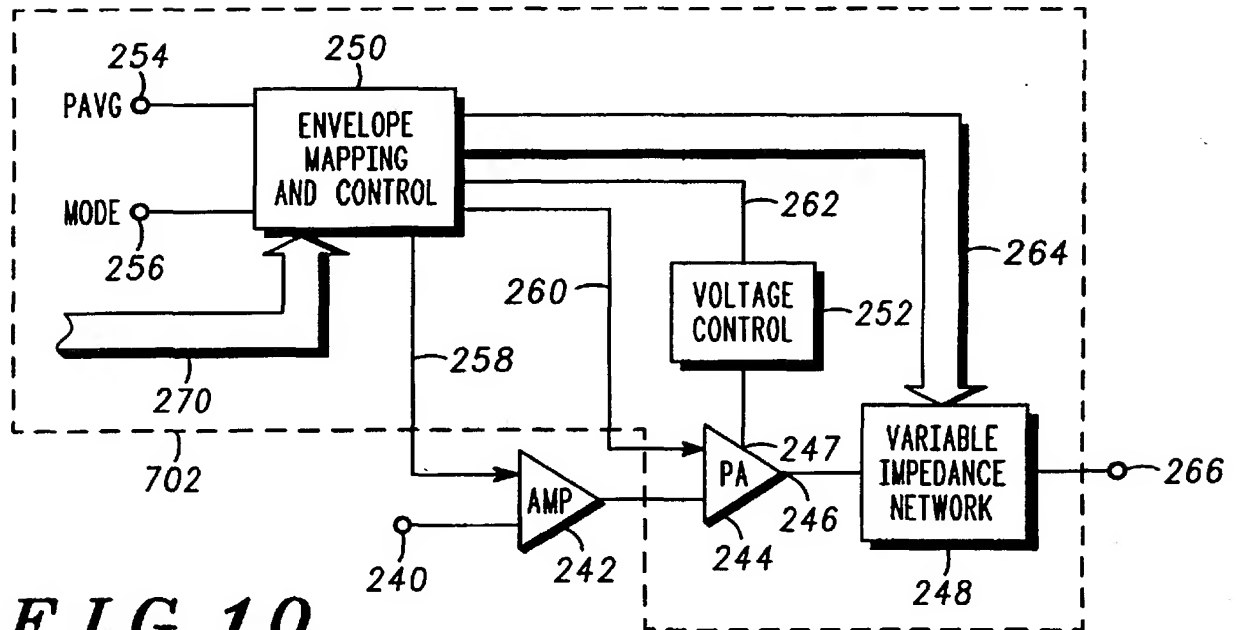


FIG. 10

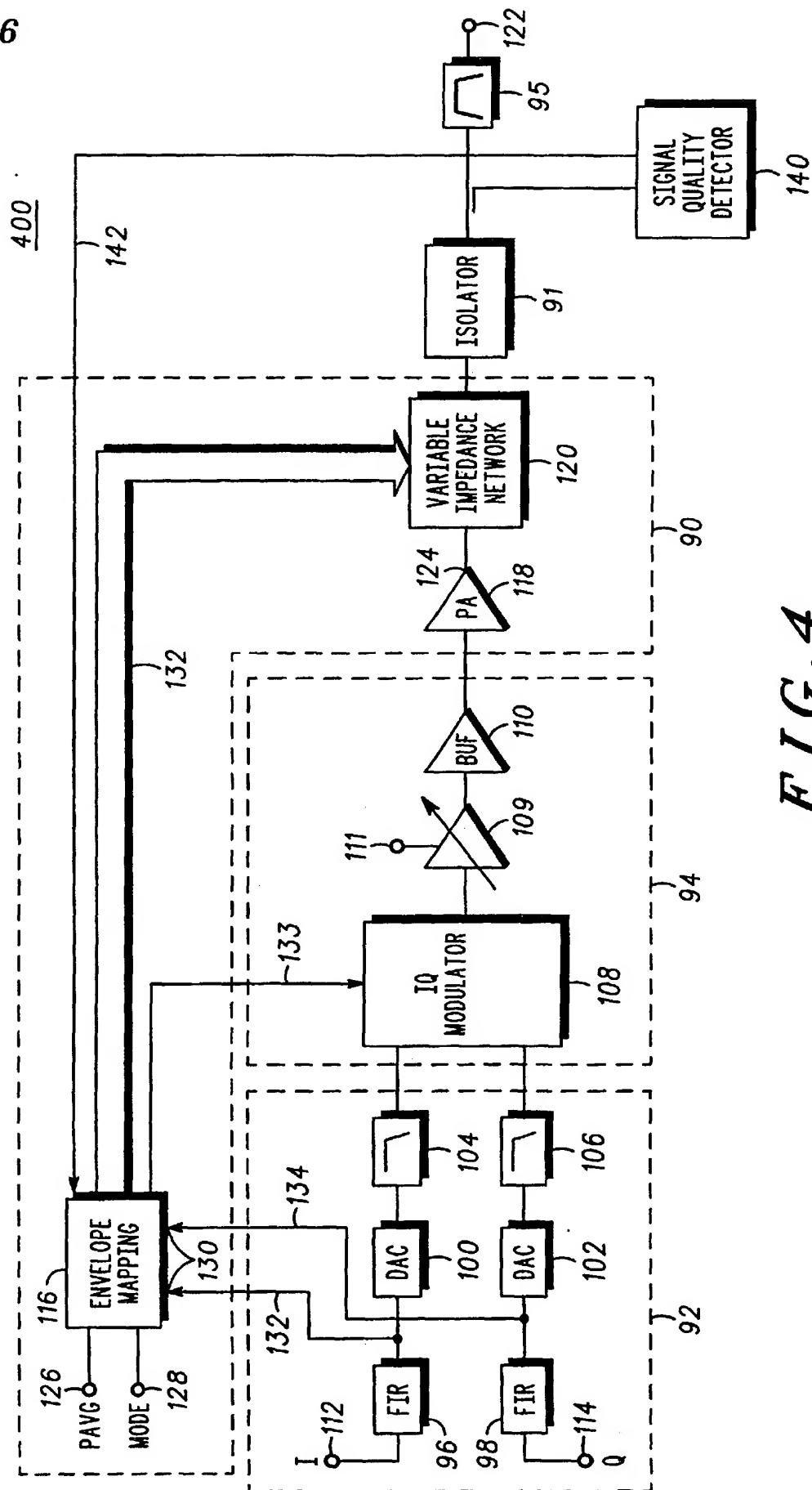


FIG. 4

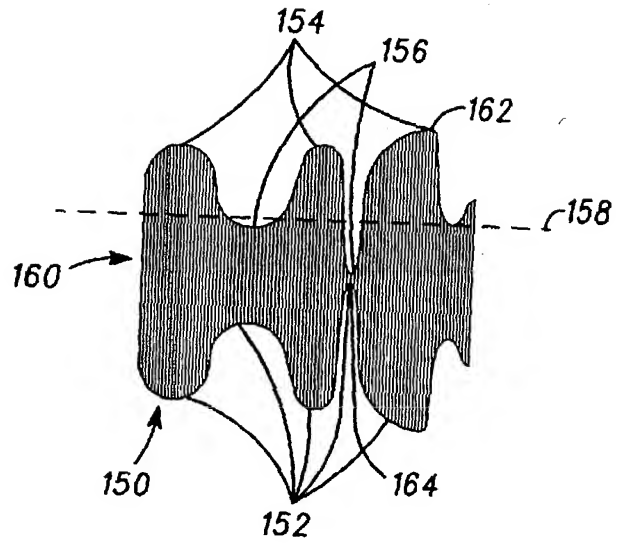


FIG. 5

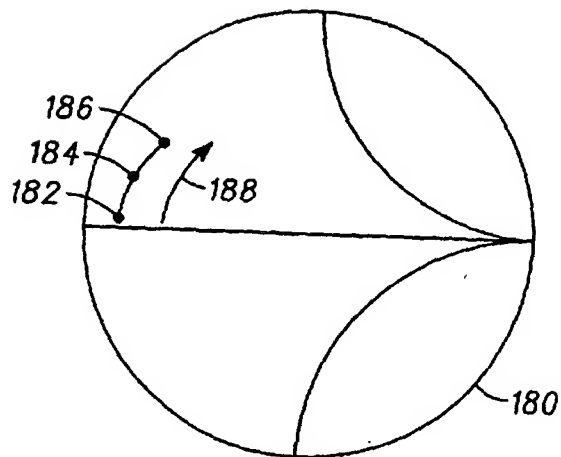


FIG. 6

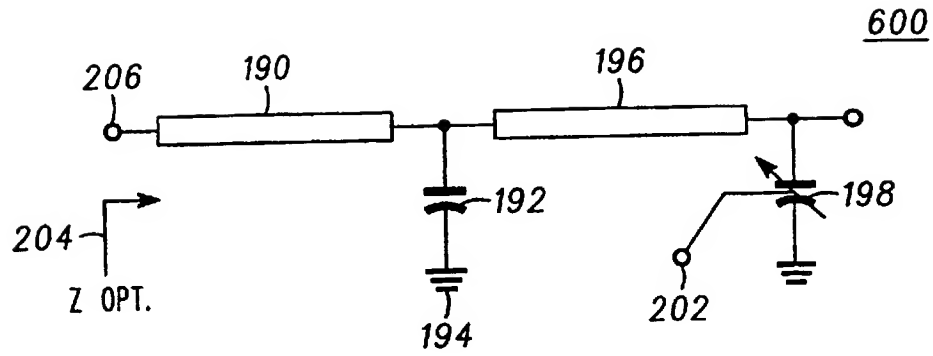


FIG. 7

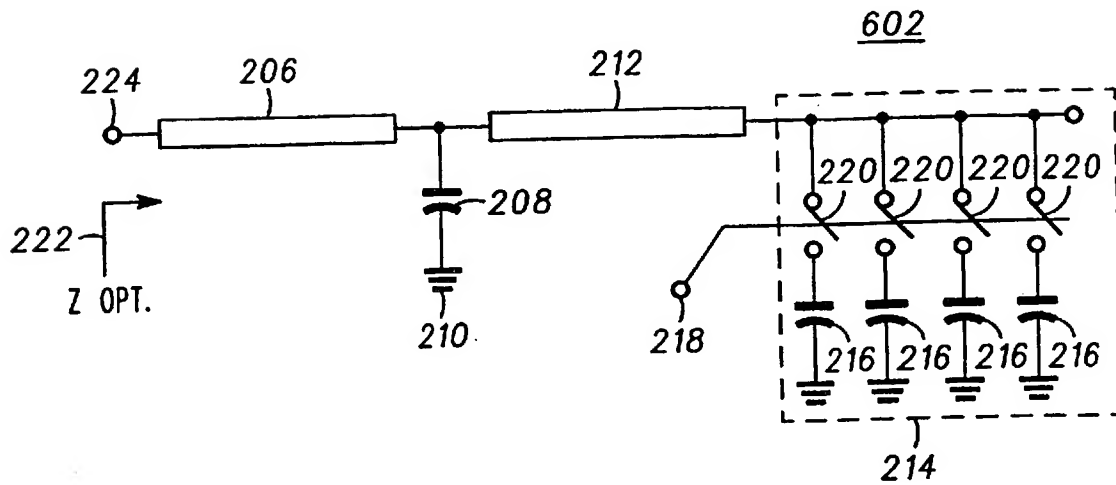
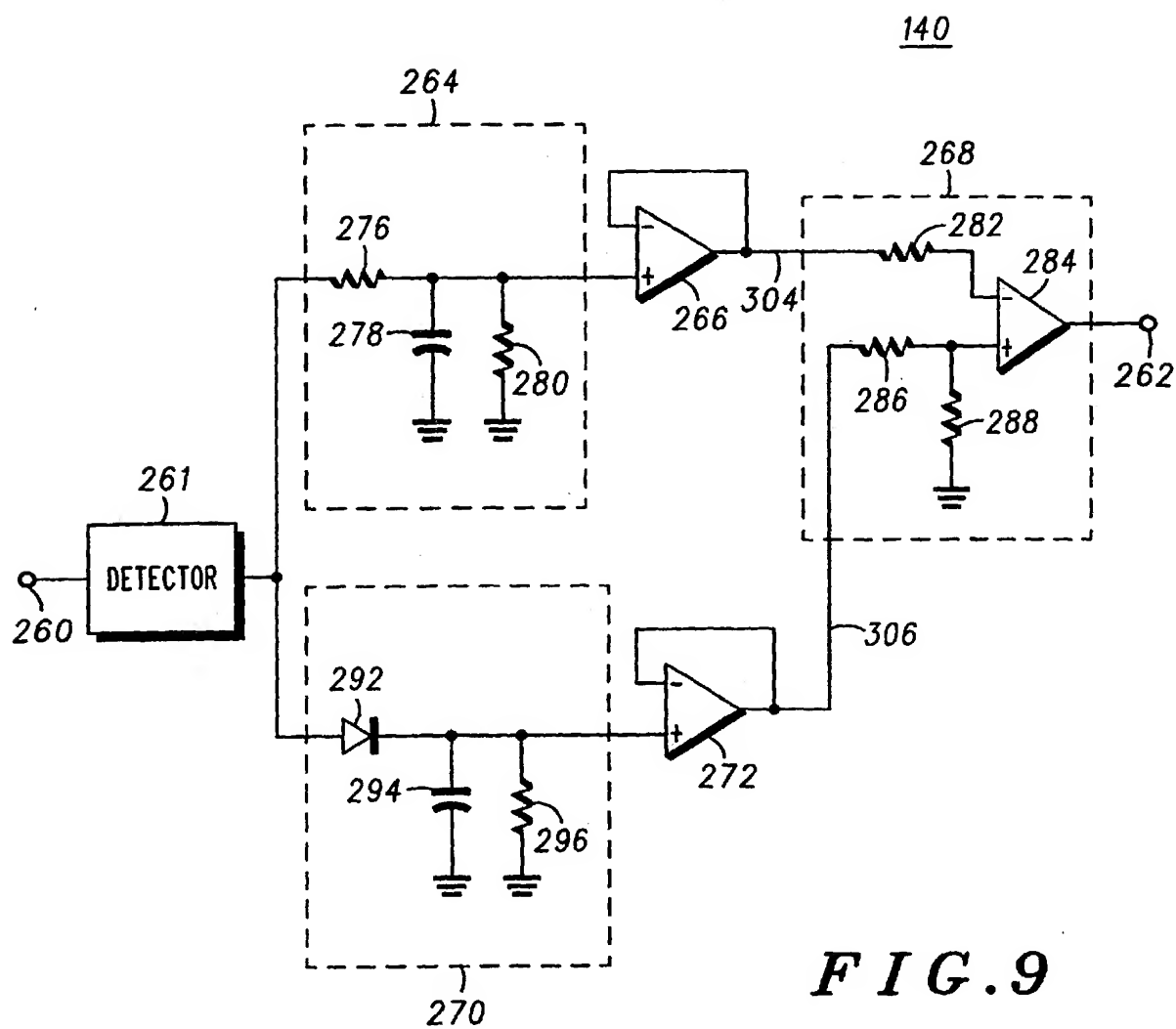


FIG. 8



LOAD ENVELOPE FOLLOWING AMPLIFIER SYSTEM

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FIELD OF THE INVENTION

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This invention generally relates to a high efficiency power amplifier system. More specifically, this invention relates to a load envelop following (LEF) amplifier system for efficient amplification in a linear modulation scheme.

BACKGROUND OF THE INVENTION

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The rapid and great increase in the cellular phone use has resulted in a shortage of frequency spectrum for handling all of the cellular customers. This shortage has prompted a migration towards more spectrally efficient, digital modulation schemes that have higher user capacities. Examples of such modulation schemes include Interim Standard (IS)-136 Time Division Multiple Access (TDMA), Personal Digital Cellular, IS-95 based Code Division Multiple Access, Global System for Mobile communications (GSM) and TDMA Edge. In addition, newer and even higher data rate capacity systems are being proposed and developed for third generation cellular systems.

The digital cellular systems usually require linear modulation, and while the linear modulation does facilitate higher system capacity, it also results in lower efficiency power amplifiers. Power amplifiers continue to consume a significant percentage of the overall current drain of the portable radiotelephones used in cellular communication systems. The higher the current drain, the less talk time and stand-by time that is available, since the battery for the portable radiotelephone will be drained faster. Thus, the efficiency of the power amplifiers used in portable radiotelephones is a critical parameter; the higher the efficiency, the less current the power amplifier consumes and the more talk/stand-by time is available for the portable radiotelephone. Several techniques have been proposed to improve the efficiency of power amplifiers linear modulation systems.

One technique for improving the efficiency of power amplifiers is Envelope Elimination and Restoration (EER). EER was first proposed by Leonard Kahn in 1952 (July Proceedings of the I.R.E. page 803-806). An example block diagram of a power amplifier (PA) EER system is depicted in FIG. 1. The PA EER system 100 includes a limiter 2 coupled to a power amplifier 4, and the power amplifier 4 coupled to antenna 14. The PA EER system 100 further includes a voltage supply control circuit 6 coupled to the power amplifier 4 through a filter 8.

In a many digital cellular systems, the information bearing radio frequency signal contains both amplitude modulation (AM) and phase modulation (PM) components. An RF signal with AM and PM components appearing at input 12 is processed by the limiter 2. The limiter 2 removes all AM information and passes a constant envelope signal to the power amplifier 4. The limiter can be a simple limiting RF amplifier.

In addition, an envelope signal appearing at input 16 contains information about the RF signal envelope of the RF signal that appears at input 12. The envelope signal is applied to the voltage supply control circuit 6. Finally, a substantially constant supply voltage is applied to the voltage supply control circuit 6 at input 10. The voltage supply control circuit 6 along with filter 8 comprises a switching power supply that modulates the supply voltage appearing at input 10 in response to the envelope signal appearing at input 16. The voltage supply control circuit 6 and the filter 8 thus supply a varying voltage signal on line 18 that varies according to the AM envelope of the RF signal originally appearing at input 12. By modulating the supply voltage to the power amplifier, the desired AM envelope is impressed on the output signal of the power

amplifier, and the resultant signal with the restored AM envelope is transmitted through antenna 14. Since the AM of the RF input signal appearing at input 12 is removed, the PA EER system 100 allows the power amplifier 4 to operate as a very efficient class C amplifier. All of the AM information is impressed upon the output signal of the power amplifier by the changes in the power amplifier supply voltage appearing on line 18.

There are several difficulties and shortcomings of the PA EER system 100. First, limiters that sufficiently remove the AM of the RF input signal are difficult to realize. The difficulty increases as the operating frequency increases. Second, the voltage supply control circuit 6 and the filter 8 are substantially a switching power supply. These circuits typically consume much power that ultimately factors into and subtracts from the overall efficiency of the PA EER system. Third, it is difficult to develop a voltage supply control circuit 6 that meets the bandwidth requirements necessary for the voltage supply control circuit 6 to follow the AM envelope of wide band systems such as code division multiple access (CDMA) cellular telephone systems. For example, the envelope signal appearing at input 16 is generated by decomposing the RF signal appearing at input 12 into separate AM and PM signals. The decomposed AM signal has significantly higher bandwidth than the composite signal, and the voltage supply control must operate at the bandwidth of the decomposed AM signal. For many of the third generation cellular systems, switching speeds in the range of approximately 20MHz could be required for such a PA EER system. And finally, the PA EER system 100 is expensive to produce. The filter 8 requires several components including at least one large inductor. These parts tend to add considerable cost and size to the portable radiotelephone, and since portable radiotelephones are many times a commodity product, price is a critical parameter.

Another technique for improving the efficiency of power amplifiers is called Envelope Following (EF). A simplified block diagram of an EF system is shown in Figure 2. The PA EF system 200 includes a power amplifier 30 coupled to antenna 40 in the RF path and a voltage supply control circuit 32 coupled through filter 34 to the power amplifier 30 on the supply path.

An RF signal having AM and PM modulation components appearing at input 38 is applied to the power amplifier 30. In addition, an envelope signal containing information about the AM envelope of the RF signal at input 38 is applied through input 42 to the voltage supply control circuit 32. Once again, the voltage supply control circuit

32 and the filter 34 are in essence a switching power supply. A substantially constant supply voltage is applied to the voltage supply control circuit 32 through input 36.

In this system, the AM is not removed from the RF signal appearing at input 38. Rather, the supply voltage supplied to the PA 30 through line 44 is reduced or increased responsive to the amplitude of the AM envelope. The supply voltage applied to the power amplifier 30 thus follows the AM envelope of the RF signal applied to the power amplifier 30. By modulating the supply voltage to the power amplifier 30 to follow the AM envelope, less power is consumed overall. For example, when the envelope is at a peak, the supply voltage to the power amplifier 30 is increased, but when the envelope is at a minimum, the supply voltage decreases, thereby saving power. This significantly increases the efficiency of the power amplifier 30.

Since the actual AM on the output signal produced by the power amplifier 30 does not originate in the voltage supply control circuit 32, some of the bandwidth requirements on the voltage supply control circuit 32 and filter 34 are reduced relative to the PA EER 100. This system also eliminates the need for a high frequency limiter. However, PA EF system 200 still requires a voltage supply control circuit 32 and filter 34, which adds significant cost and size to a portable radiotelephone.

Accordingly, there is a need for power amplifier systems operable in linear modulation systems. There is a further need for these linear power amplifier systems to be efficient so as to minimize current drain for operation in portable radiotelephones. There is a further need to minimize the cost of producing linear power amplifier systems in order to make the portable radiotelephones that utilize the power amplifier systems price competitive.

Brief Description of the Drawings

FIG. 1 is a block diagram of a prior art envelope elimination and restoration (EER) system;

FIG. 2 is a block diagram of a prior art envelope following (EF) system;

FIG. 3 is a block diagram of a radiotelephone having a receiver and a transmitter;

FIG. 4 is a block diagram of a load envelope following (LEF) system for use in the transmitter of FIG. 3;

FIG. 5 shows the RF modulated signal produced by the LEF system of FIG. 4; and

FIG. 6 is a Smith Chart showing three different optimum impedances to be presented to a power amplifier used in the LEF system of FIG. 4 corresponding to three instantaneous power levels;

FIG. 7 shows a variable impedance network that can be used to present various impedances to the power amplifier;

FIG. 8 shows a second embodiment variable impedance network;

FIG. 9 is an example embodiment of a signal quality detector used in conjunction with the PA LEF system; and

FIG. 10 is a block diagram exemplifying an alternate embodiment LEF system.

Detailed Description of Preferred Embodiments

FIG. 3 is an illustration in block diagram form of a radiotelephone communication system 300. The radiotelephone communication system 300 includes a remote transceiver 60 and one or more radiotelephones such as radiotelephone 62. The remote transceiver 60 sends and receives RF signals to and from the radiotelephone 62 within a designated geographic area.

The radiotelephone 62 includes an antenna 64, a transmitter 66 (also referred to as a radio frequency transmitter), a receiver 68, a control block 70, a synthesizer 72, a duplexer 74, and a user interface 76. To receive information, the radiotelephone 62 detects RF signals containing data through the antenna 64 and produces detected RF signals. The receiver 68 converts the detected RF signals into electrical baseband signals, demodulates the electrical baseband signals, recovers the data, including automatic frequency control information, and outputs the data to the control block 70. The control block 70 formats the data into recognizable voice or data information for use by the user interface 76. Typically the user interface 76 includes a microphone, a speaker, a display, and a keypad, and the user interface 76 is for receiving user input information and presenting received data that was transmitted by remote transceiver 60. The receiver includes circuitry such as low noise amplifiers, filters, down conversion mixers and quadrature mixers, and automatic gain control circuitry, all known in the art.

To transmit RF signals containing information from the radiotelephone 62 to the remote transceiver 60, the user interface 76 directs user input data to the control block 70.

The control block 70 typically includes any of a DSP core, a microcontroller core, memory, clock generation circuitry, software, and a power amplifier control circuit. The control block 70 formats the information obtained from the user interface 76 and conveys it to the transmitter 66 for conversion into RF modulated signals. The transmitter 66 conveys the RF modulated signals to the antenna 64 for transmission to the remote transceiver 60. Thus, the transmitter 66, also referred to as an RF transmitter, is for transmitting a modulated information signal. The duplexer provides isolation between the signals transmitted by the transmitter 66 and received by the receiver 68.

The synthesizer 72 provides the receiver 68 and the transmitter 66 with signals, tuned to the proper frequency, to allow the reception and transmission of information signals. Control over functions of the receiver 68 and the transmitter 66, such as channel frequency, is provided by the control block 70. Thus, the control block 70 provides the synthesizer 72 program instructions for frequency synthesis.

FIG. 4 is a block diagram of a transmitter that can be used for the radiotelephone 62. The transmitter 400 employs a power amplifier load envelope following (PA LEF) system 90 for efficient operation in a linear modulation scheme.

In FIG. 4, the transmitter 400 is partitioned into base-band circuitry 92, radio frequency (RF) circuitry 94, the PA LEF system 90, and a filter 95. The base-band circuitry 92 and the RF circuitry 94 can be collectively referred to as transmitter circuitry. The filter 95 is of conventional design and may alternatively be incorporated into the duplexer 74 (FIG. 3).

The base-band circuitry 92 includes a first finite impulse response (FIR) filter 96, a second FIR filter 98, a first digital-to-analog (DAC) converter 100, a second DAC 102, a first anti-alias filter 104, and a second anti-alias filter 106. All of the circuits shown as the baseband circuitry 92 in FIG. 4 are of conventional design. Other configurations of the baseband circuitry 92 can be utilized without the use of the inventive faculty. Further, at least a portion of the baseband circuitry can be realized using circuitry within the control block 70 of FIG. 3.

The RF circuitry 94 includes an in-phase and quadrature phase (IQ) modulator 108 coupled to a variable gain element, here variable gain amplifier (VGA) 109. The VGA 109 is coupled to a buffer amplifier 110. The IQ modulator contains a mixer pair that receives an in-phase baseband signal (I) appearing at first baseband input 112, a quadrature-phase baseband signal (Q) appearing at second baseband input 114, an in-

phase intermediate frequency (IF) carrier signal (not shown), and a quadrature phase IF carrier signal (not shown). The IQ modulator also contains an up-conversion mixer coupled to the mixer pair for converting the IF modulated signal to an RF signal. The IQ modulator 108, the VGA 109, and the buffer amplifier 110 are also of conventional design.

The PA LEF system 90 includes an envelope mapping circuit 116, a power amplifier 118, and a variable impedance network 120. The power amplifier 118 can be a single stage amplifier or a multi-stage amplifier.

The path from the first baseband input 112 and the second baseband input 114 to the RF output 122 is typically referred to as the RF path. Modifications and/or additions to the RF path can be made. For example, in the illustrated embodiment an upconversion mixer with associated IF filters are included in the RF path (e.g. within the IQ modulator 108) for frequency selectivity. Alternatively, the IQ modulator can convert baseband signals directly to the transmit RF, thereby forming a direct launch transmitter.

Circuitry within the control block 70 (FIG. 3) converts user input to a digital I and Q signal that are applied to the first baseband input 112 and the second baseband input 114, respectively. The first FIR filter 96 and the second FIR filter 98 filters the I and Q signals to reduce the inter-symbol interference that would otherwise result from transmission over a transmission channel/medium. The first DAC 100 and the second DAC 102 convert the digital I and Q signals into I and Q analog signals. First anti-alias filter 104 and second anti-alias filter 106 filter the I and Q analog signals as is known in the art.

The IQ modulator 108 receives the I and Q analog signals, and quadrature modulates them to an IF modulated signal. An up-conversion mixer within the IQ modulator 108 then converts the IF modulated signal to an RF modulated signal. Both the IF modulated signal and the RF modulated signals typically contain amplitude modulation (AM) envelope and phase modulation (PM) in accordance with the modulation scheme utilized.

The RF modulated signal is applied to the VGA 109. In addition, average transmit power control circuitry (not shown) is coupled to the control block 70, the transmitter circuitry, and the PA LEF system 90. The average transmit power control circuitry is for generating an automatic output control (AOC) signal as is known in the art. In the illustrated embodiment, the control block 70 (FIG. 3) contains the average

transmit power control circuitry. The VGA 109 receives at VGA control input 111 the AOC signal for setting the gain of the VGA 109. By varying the gain of the VGA 109, the radiotelephone 62 (FIG. 3) can vary its average transmitted output power (and thus the average amplitude of the modulated signal).

5 In an alternative embodiment, the VGA 109 is a multi-stage variable gain amplifier so that several stages of gain can be altered. The multiple stages of variable gain amplification can also be distributed along different portions of the transmit path. Further, at least some of the necessary variable gain can be achieved through the use of a variable attenuator rather than a variable gain amplifier as is known in the art.

10 The control block 70 can generate the AOC signal in response to radiotelephone 62 making received signal strength measurements (e.g. open loop power control). The remote transceiver 60 can also send the radiotelephone 62 a power control command, and the control block 70 generates the AOC signal at least in part responsive to the remote transceiver 60 power control command (e.g. IS-95 CDMA closed loop power control). In addition, the control block 70 can generate the AOC signal using a combination of open loop and closed loop power control.

15 The RF signal produced by the VGA 109 is coupled to the buffer amplifier 110 for signal amplification. This amplifier increases the maximum gain through the RF path so that a maximum necessary output power can be achieved when necessary. As at the output of the IQ modulator 108, the buffered signal contains an amplitude modulation (AM) envelope and phase modulation (PM) in accordance with the modulation scheme utilized. The buffered signal is coupled to the power amplifier 118 as a power amplifier input signal. The power amplifier 118 produces an amplified version of the power amplifier input signal at power amplifier output 124. The supply voltage (not shown) applied to the power amplifier 118 is substantially constant in that the power amplifier supply voltage is not purposely varied in response to the AM envelope. Thus, the power amplifier 118 is biased to have sufficient current and voltage headroom in order to provide substantially linear amplification at even the highest output power levels.

20 With the substantially constant bias applied to the power amplifier 118, the variable impedance network 120 provides a variable load impedance to the power amplifier output 124 to alter the load line of the power amplifier 118 in response to the envelope of the transmitted signal. This increases the overall efficiency of the power amplifier 118 and reduces dissipated power.

Envelope mapping circuit 116 is a signal processor and can be realized with, for example, a DSP or an application specific integrated circuit (ASIC). Envelope mapping circuit technology is generally known to those skilled in the art. An example of an envelope mapping circuit is described in U.S. patent 5,420,536. Other envelope mapping circuit implementations can be utilized as is known in the art.

An average power level signal generated by the control block 70 (FIG. 3), designated P_{AVG} in FIG. 4, is applied to power level input 126. The average power level signal contains information as to the desired transmit power level and is thus a derivative of the AOC signal.

The average power level signal is used by the envelope mapping circuit 116 for a mapping process. For a particular output power level, there exists an optimum output impedance for the power amplifier 188 to have the highest efficiency. When the transmit power level is changed by varying the gain of the VGA 109, a different optimum impedance must be presented to the power amplifier output 124. Thus, the envelope mapping circuit 116 must produce a load control signal on bus 132 responsive not only to the amplitudes of the I and Q signals, but also responsive to the average transmit power level.

For example, when the radiotelephone 62 is used for an IS-95 code division multiple access (CDMA) cellular telephone system, the average transmit power must vary from -50 dBm to +24 dBm in 1 dB steps. This dynamic range is achieved at least in part by the VGA 109. However, because of the OQPSK modulation in IS-95, the transmitted signal has an AM envelope with an amplitude that varies from -40 dB to 5.6 dB, with 0 dB variance being the median output level for a particular average output power setting. Thus, if the transmit power level is set to 24 dBm, the amplitude of the modulation envelope varies from -16 dBm to 29.6 dBm. A group of settings are contained in the envelope mapping circuit 116 so that the load control signal can adjust the variable impedance network 120 in response to I and Q signals detected on lines 132 and 134, respectively. These settings are specific to detected I and Q values when the transmit power level is 24 dBm.

When a different average transmit power is selected, such as -50 dBm, the average power level signal appearing at power level input 126 reflects this change. With the OQPSK modulation, the transmitted signal now has an AM envelope with an amplitude that varies from -90 dBm to -44.4 dBm. The envelope mapping circuit 116

now produces a load control signal on bus 132 to adjust the variable impedance network 120 responsive to the amplitude and phase of the I/Q signals and the new transmit power level.

5 The control block 70 (FIG. 3) also generates a mode signal that is applied to the mode input 128 of the envelope mapping circuit 116. The mode signal is for selecting the mode of operation of the transmitter 400. For example, the radiotelephone 62 can be a multi-mode phone operable in a CDMA mode and an AMPS mode. The mode signal causes the envelope mapping circuit 116 to produce the load control signal on bus 132 that causes the variable impedance network 120 to operate in accordance with the
10 particular mode that the radiotelephone 62 is operating in.

If the radiotelephone 62 is operating in the AMPS mode, the load control signal on bus 132 would be dependent substantially only on the desired average transmit power level. This is because in the AMPS mode, the transmit signal is a constant envelope signal resulting from frequency modulation.

15 The envelope matching circuit 116 continuously generates a load control signal responsive to the detected amplitude and phase of the baseband I and Q signals, the desired average transmit power level, and the radiotelephone mode of operation. The load control signal thus represents, in accordance with the mode of operation selected, the dynamic amplitude and phase of the I and Q baseband signals as well as the desired
20 average transmit power level of the radiotelephone 62. The load control signal can be either a digital signal, an analog waveform, or a combination of analog and digital waveforms that contains information about the instantaneous absolute power level of the transmitter. The envelope mapping circuit 116 can perform the envelope mapping function algorithmically or as a look-up table.

25 In addition, a method of transmitting user input information comprises converting the user input information into an input signal, modulating a carrier wave signal with the input signal to form a modulated signal having an amplitude modulation (AM) envelope, amplifying the modulated signal having an AM envelope with a power amplifier, and varying a load impedance at an output of the power amplifier substantially in harmony
30 with the AM envelope.

FIG. 4 shows that the transmitter 400 can optionally include an isolator 91 coupled to the output of the variable impedance network 120. The isolator presents a substantially constant impedance to the output of the variable impedance network 120.

This will provide extra predictability to the various impedances that the variable impedance network 120 provides to the power amplifier output 124.

FIG. 4 also shows the transmitter 400 can optionally have a distortion control line 133 coupling the envelope matching circuit 116 to the IQ modulator 108. This distortion control line 133 optionally allows for the introduction of predistortion onto an input signal before applying the input signal to the power amplifier 124.

The variable impedance network 120 presents various impedances to the power amplifier output to maximize power amplifier efficiency while still maintaining power amplifier linearity. Due to biasing of the power amplifier 118 and the various output loads presented, the power amplifier 118 operates as close as possible to saturation/compression to provide for maximum power amplifier 118 efficiency. Operating so close to saturation may cause the power amplifier 118 to introduce distortion onto the RF input signal. In addition, the variable impedance network 120 may introduce some level of amplitude and phase distortion to the signal. With an a priori knowledge of this distortion, either through measurements, calculations, or both, predistortion can be impressed upon the signal to be amplified in order to account for the distortion resulting from the power amplifier 118 and/or the variable impedance network 120.

The envelope mapping circuit 116 also contains distortion control circuitry for generating the distortion control signal on distortion control line 133. The distortion control signal can be an analog signal or a digital signal. In addition, the distortion control circuitry can be in a different portion of the radiotelephone 62 rather than within the envelope mapping circuit 116. For example, the distortion control circuitry can be within the control block 70 (FIG. 3).

FIG. 4 shows the distortion control line 133 coupling the envelope mapping circuit 116 to the IQ modulator 108 so that circuitry within the IQ modulator 108 introduces the pre-distortion signal. Alternatively, the distortion control signal can be applied to a different portion of the transmitter 400 circuitry, such as baseband circuitry 92 (FIG. 4) or circuitry within control block 70 (FIG. 3). The pre-distortion could then be introduced via the baseband circuitry 92 or via circuitry within the control block 70 (FIG. 3).

Thus, the radiotelephone 62 has digital processing circuitry (either within the envelope mapping circuit 116 of FIG. 4, the control block 70 of FIG. 3, or elsewhere

within the radiotelephone 62) for converting user input information to an input signal. The radiotelephone 62 also includes transmit circuitry (here baseband circuitry 92 and/or RF circuitry 94) coupling the digital processing circuitry to the input of the power amplifier 118 for amplifying and modulating the input signal before applying the input signal to the signal amplifier. In addition, the radiotelephone 62 includes distortion control circuitry coupled to any of the digital processing circuitry, the transmit circuitry, and the envelope mapping circuit 116. The distortion control circuitry causes any of the digital processing circuitry and the transmit circuitry to introduce pre-distortion upon the input signal before applying the input signal to the power amplifier 118. The distortion control circuitry can be realized using a DSP, a micro-controller, or other circuitry.

Alternatively stated, distortion control circuitry is coupled to the envelope mapping circuit 116 and the transmit circuitry (here baseband circuitry 92 and/or RF circuitry 94). The distortion control circuitry is for generating a distortion control signal and coupling the distortion control signal to the transmit circuitry to cause the transmit circuitry to impress pre-distortion upon the input signal before applying the input signal to the power amplifier 118.

The introduction of pre-distortion is generally known to those skilled in the art. As an example, see U.S. patent 5,113,414. Other methods and systems to introduce pre-distortion can be utilized as is known in the art.

Interpolation can be used in the envelope mapping circuit 116 to generate a load control signal that causes the variable impedance network 120 to vary over a substantially continuous range of impedances. Alternatively, the load control signal causes the variable impedance network 120 to vary in discrete steps the various impedances presented to the power amplifier output 124.

FIG. 4 also shows the transmitter 400 can optionally include a signal quality detector 140 coupled to the power amplifier output 124. Signal quality control line 142 directs a signal quality indication to the envelope mapping circuit 116. The signal quality detector 140 measures the peak to average ratio of the RF signal produced at the power amplifier output 124.

The output of the signal quality detector 140 is used by the envelope mapping circuit 116 to compensate for variances in circuitry in the RF path. For example, the power amplifier 118 performance varies over temperature. The power amplifier performance 118 also varies from part to part within different radiotelephones. The

variable impedance network 120 performance may also vary due to temperature variations and part variations. All of these variations must be accounted for by setting the operating point of the power amplifier 118 to account for worst case conditions. For example, the power amplifier 118 might be biased to have a higher quiescent current to maintain linearity over worst case temperature and part variation conditions than would otherwise be necessary if no performance variations were possible. Having to account for these worst case conditions means the power amplifier 118 may have to be biased to expend additional DC power to maintain linearity over possible worst case conditions.

The inclusion of the signal quality detector 140 helps account for performance variations and thereby allows the power amplifier 118 used within each radiotelephone to operate closer to saturation, thereby maximizing power amplifier efficiency. The signal quality detector 140 monitors the peak to average ratio of the RF signal. Alternatively, the signal quality detector 140 can measure the amount of adjacent channel power (ACP) that the radiotelephone 62 (FIG. 3) transmits. Further details on the measuring of adjacent channel power are described in a co-pending U.S. patent application entitled "OFF-CHANNEL LEAKAGE POWER MONITOR APPARATUS AND METHOD," Ser. No. 08/968,625, filed Nov. 12, 1997, assigned to the assignee of the present invention, the disclosure thereof incorporated by reference. The signal quality detector 140 sends an indication of the peak to average ratio via signal quality control line 142 to the envelope mapping circuit 116. The envelope mapping circuit 116 uses the signal quality indication to adjust the variable impedance network 120, and the variable impedance network 120 controls the impedances presented to the power amplifier output 124 to maintain a constant peak to average ratio in the RF signal.

Varying the impedances presented to the power amplifier output 124 can be understood more with reference to FIGs. 5 and 6. FIG. 5 shows the RF modulated signal 150 that is produced at the power amplifier output 124 of FIG. 4. The input signal to the power amplifier 118 (FIG. 4) has an AM envelope, and the RF modulated signal 150 (FIG. 5) is an amplified version of the input signal having an AM envelope 152. Due to the various linear modulation schemes, the amplitude of the AM envelope continuously varies and thus produces peaks 154 and valleys 156. The average transmit power level, as indicated by the dashed line 158, is in between the peaks 154 and the valleys 156. The modulation is carried by an RF carrier signal 160.

As discussed previously, the variable impedance network 120 (FIG. 4) is continuously adjusted to present a substantially optimum impedance to the power amplifier output 124 (FIG. 4). This maximizes the power amplifier 118 efficiency for the different amplitude excursions along the AM envelope 152 (FIG. 5) and for the different average transmit power levels.

FIG. 6 shows a Smith Chart showing three different optimum impedances to be presented to the power amplifier output 124 (FIG. 4) corresponding to three instantaneous power levels. Thus first impedance point 182, second impedance point 184, and third impedance point 186 represent impedances at a single carrier frequency but at different power levels at the power amplifier output 124. First impedance point 182 corresponds to peak power point 162 (FIG. 5), second impedance point 184 corresponds to average transmit power level (indicated by dashed line 158 of FIG. 5), and third impedance point 186 corresponds to minimum power point 164. By continuously varying the load impedance presented to the power amplifier output 124, the load impedance corresponding to the best power amplifier 118 efficiency can be continuously presented to the power amplifier 118 as the amplitude of the AM envelope 152 changes. The overall efficiency of the power amplifier 118 is thus increased.

Referring to FIGs. 5 and 6, the second impedance point 184 corresponds to average power point 163 (FIG. 5). If, due to temperature and/or part variations, a measured peak to average ratio is greater than a predetermined, desired, peak to average ratio, a different impedance is selected along the impedance set denoted by impedance points 182 through 186. A different impedance is selected by moving along the impedance set on the Smith chart in the direction shown by direction arrow 188. Thus, instead of using second impedance point 184 for a certain power level, the third impedance point 186 is used. This causes the power amplifier 118 to operate closer to saturation, thereby increasing amplifier efficiency. Likewise, if the peak to average ratio is less than desired, a different impedance is selected by moving along a set of impedances in a direction opposite the direction arrow 188, as shown on the Smith Chart 180 of FIG. 6.

The signal quality detector can be realized with diode detection circuitry and resistor-capacitor networks. FIG. 4 shows the signal quality detector sending a signal quality indication via signal quality control line 142 directly to the envelope mapping circuit 116. Alternatively, the signal quality indication can be coupled to other circuitry

for processing, such as control block 70 (FIG. 3), and then coupled to envelope mapping circuit 116. Still further, analog circuitry such as operational amplifier circuitry (not shown) can receive the load control signal present on bus 132 and the signal quality indication present on signal quality control line 142. The analog circuitry can then
5 process the two signals and generate a modified load control signal that is applied to the variable impedance network 120.

FIG. 7 shows a first embodiment variable impedance network 600 that can be used to present various impedances to the power amplifier 118 (FIG. 4). The variable impedance network 600 includes first transmission line 190, fixed shunt capacitor 192
10 coupled to ground potential 194, second transmission line 196, and at least one variable element 198 coupled to ground potential 194. The variable element 198 can be selected from the group consisting of a varactor diode and a voltage variable capacitor. The load control signal is applied to input 202 as a voltage to vary the capacitance of the variable element 198. Arrow 204 shows that the impedance presented at input 206 is maintained
15 to be substantially the optimum impedance for the power amplifier 118 (FIG. 4) for the various output power levels.

Other configurations of the variable impedance network 600 can be envisioned. For example, additional elements can be included, such as lumped element or distributed element inductors, additional transmission lines and capacitors, and additional variable
20 elements.

FIG. 8 shows a second embodiment variable impedance network 602. The variable impedance network 602 includes first transmission line 206, a fixed shunt capacitor 208 coupled to ground potential 210, second transmission line 212, and at least one variable element 214 coupled to ground potential 210. The variable element
25 comprises a plurality of capacitors 216 alternatively coupled and uncoupled to the power amplifier output 124 using PIN diodes or micro-mechanical switches. The load control signal is applied to input 218 to open and close a plurality of switches 220 coupling the plurality of capacitors 216 to the power amplifier output 124. Arrow 222 shows that the impedance presented at input 224 is maintained to be substantially the optimum
30 impedance for the power amplifier 118 (FIG. 4) for the various output power levels.

Thus, the variable impedance network has at least one variable element. The variable element can be selected from the group consisting of a varactor diode, a voltage

variable capacitor, and a plurality of capacitors alternatively coupled and uncoupled to the output of the signal amplifier using micro-electromechanical switches or PIN diodes.

Other configurations of the variable impedance network 602 can be envisioned without the use of the inventive faculty. For example, additional elements can be included, such as lumped element or distributed element inductors, additional transmission lines and capacitors, and additional variable elements. A combination of the variable impedance network 600 and the variable impedance network 602 could be used to increase the range of obtainable impedances. In addition, the load control signal can comprise multiple signals to separately control different variable impedance elements.

FIG. 9 is an example embodiment of a signal quality detector 140 used in conjunction with the PA LEF system 90. The signal quality detector 140 includes a conventional envelope detector 261, an averaging circuit 264, a first op-amp 266, a difference circuit 268, a peak detector 270, and a second op-amp 272.

The averaging circuit 264 includes a series resistor 276 coupled to a shunt capacitor 278 and a shunt resistor 280. The peak detector 270 includes a diode detector 292 coupled to a shunt capacitor 294 and a shunt resistor 296.

The difference circuit 268 has a first input 304 coupled through a series resistor 282 to the negative input of an op-amp 284. A second input 306 is coupled through a series resistor 286 and a shunt resistor 288 to a positive input of the op-amp 284. The output of the op-amp 284 is the signal quality detector output 262.

A sample of the output signal is applied to signal quality detector input 260, and the conventional envelope detector 261 produces a detected baseband signal with an AM envelope. The detected baseband signal is applied to the averaging circuit 264 and the peak detector circuit 270. The peak of the detected baseband signal is produced by the peak detector 270, whereas an average value of the detected baseband signal is produced by the averaging circuit 264.

First op-amp 266 and second op-amp 272 are voltage followers to provide isolation between the averaging circuit 264 and the difference circuit 268 and between the peak detector 270 and the difference circuit 268. The buffered average value is applied to first input 304 and the buffered detected peak of the signal is applied to second input 306.

The difference between the peak and average signal is produced at output 262, which is then sampled by an analog-to-digital (A-D) converter (not shown). The A-D

converter can be within the envelope mapping circuit 116 (FIG. 4), within the control block 70 (FIG. 3), or elsewhere within the radiotelephone 62. The sampled difference is compared to a correlation table that gives the value of the peak-to-average ratio for the envelope. This sampling takes place at a very low rate since the sampling is of an averaged value. The envelope mapping circuit 116 adjusts the load impedance presented by the variable impedance network 120 until the monitored peak-to-average ratio is substantially equal to a pre-determined value.

In an alternate embodiment, the PA LEFF system 90 (FIG. 4) responds to only a portion of the AM envelope. This can be done to reduce the range of impedances necessary to follow the AM envelope. As an example, only the top 8 dB of the AM envelope is followed, so that the variable impedance network 120 presents impedances to the power amplifier 118 responsive to only the top 8 dB of the AM envelope.

Much of the high frequency amplitude information is contained in the valleys (e.g. minimums) 156 (FIG. 5) of the AM envelope. By using the PA LEF system to respond to only a portion of the AM envelope, the bandwidth of the load control signal can be significantly reduced. In addition, by using the PA LEF system to respond to only a top portion of the AM envelope, the power amplifier performance can be improved at the higher output powers, where the power amplifier draws the most current.

FIG. 10 shows a block diagram exemplifying a portion of a transmitter 700 incorporating an alternate embodiment PA LEF system 702. A driver amplifier 242 is coupled to power amplifier 244. The power amplifier output 246 is coupled to a variable impedance network 248. An envelope mapping and control circuit 250 is coupled to the driver amplifier 242, the power amplifier 244, and the variable impedance network 248. The envelope mapping and control circuit 250 is also coupled to the supply port 247 of the power amplifier 244 via a voltage control circuit 252.

An input signal having an AM envelope is applied to the system input 240, an indication of the average transmit power is applied to the envelope mapping and control circuit 250 via power level input 254, and an indication of the mode of operation is applied to the envelope mapping and control circuit 250 via mode input 256. In addition, a signal responsive to and indicative of amplitude and phase variations of the I and Q signals is applied to the envelope mapping and control circuit 250 via bus 270. The envelope mapping circuit 250 produces a load control signal on bus 264 that is responsive and indicative of the amplitude and phase variations of the I and Q signals.

The driver amplifier 242 provides preliminary signal amplification of the input signal, and the power amplifier 244 amplifies the input signal to a final transmit power level to produce an amplified signal. The variable impedance network 248 is coupled to receive the amplified signal and the load control signal. The load control signal causes the variable impedance network 248 to present various impedances to the power amplifier output 246 responsive to amplitude and phase variations of the I and Q signal.

In many applications, the transmitted output power is at the lower end of the available transmission power levels. For example, in the IS-95 CDMA cellular telephone system, the transmitted power range is +24 dBm to approximately -50 dBm. The portable radiotelephone statistically spends more time transmitting at the lower power levels than at the higher power levels.

If the supply voltage to the power amplifier is held substantially constant over the entire transmit power range, a sacrifice in power amplifier efficiency results. Lowering the supply voltage to the power amplifier 244 during lower transmit power levels, or even more aggressively during valleys in the AM envelope, will increase the overall efficiency of the system.

Thus, envelope mapping and control circuit 250 produces a supply voltage control signal on supply control line 262 in response to the signal at power level input 254. The supply voltage to the power amplifier 244 then varies with the desired average transmit power level. For example, as the transmit power increases towards the maximum value, the supply voltage at supply port 247 increases to its maximum voltage. The supply voltage control signal could also vary with the amplitude variations in the AM envelope for further increases in efficiency.

The voltage control circuit 252 is a linear or switching regulator. Since changing the supply voltage of the power amplifier 244 can affect the power amplifier 244 gain, phasing can be performed at the factory to compensate for and factor in the changes in gain.

For even more efficiency improvements, the DC currents in the driver amplifier 242 and/or the power amplifier 244 can be adjusted responsive to the desired average transmit power level. For example, as the average transmit power level is decreased, less current is needed in the driver amplifier 242 and the power amplifier 244 for them to remain linear. Therefore, the envelope mapping and control circuit 250 produces a first bias control voltage on line 258 and a second bias control voltage on line 260.

If the driver amplifier 242 is a field effect transistor (FET) amplifier, the first bias control voltage can be the gate voltage. Similarly, if the power amplifier 244 is a FET amplifier, the second bias control voltage can be the gate voltage. If bipolar devices are used, the bias control voltages can be, for example, base-emitter voltages.

5 As discussed with FIG. 4, optional features can be added to the PA LEF system 702. For example, an isolator can be placed at the output 266. In addition, pre-distortion can be added. Finally, a signal quality detector can be placed at the output to constantly monitor the output signal to maintain a predetermined peak-to-average ratio.

10 The previous description of the preferred embodiments are provided to enable any person skilled in the art to use the method of or make the apparatus for activating a spread-spectrum radiotelephone receiver. Various modifications to these embodiments will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other embodiments without the use of the inventive faculty.

15 For example, the AM envelope can result from a modulation scheme used in a cellular telephone system selected from the group consisting of a time division multiple access (TDMA) system, a code division multiple access (CDMA) system, and a combination TDMA-CDMA system. Alternatively, the PA LEF system can be used in any radio transmission system having a modulation scheme that necessitates linear amplification or that produces an AM envelope on the carrier signal.

20 Additionally, a radiotelephone could transmit at substantially a single desired average power. In that instance, the load control signal is responsive to at least a top portion of the AM envelope. The load control signal is not responsive to changes in the desired average power level.

25 The PA LEF system provides for an efficient amplification system of a RF signal. The PA LEF system provides a cost-effective method of and system for amplifying a modulated signal having an AM envelope. Use in a radiotelephone provides for significant current savings at a lower cost compared to prior art systems. With the increased complexity of future generation radiotelephones, the PA LEF system used in a radiotelephone will provide a significant performance advantage over conventional
30 radiotelephones.

We claim:

CLAIMS

- 1 1. A load envelope following (LEF) amplifier system comprising:
2 a signal amplifier having an input and an output, the input for receiving an input signal
3 having an amplitude modulation (AM) envelope, the signal amplifier for producing an
4 amplified version of the input signal;
5 a variable impedance network coupled to the output of the signal amplifier, the variable
6 impedance network for substantially continuously presenting various impedances to
7 the output of the signal amplifier responsive to a load control signal; and
8 an envelope mapping circuit coupled to the variable impedance network and for
9 producing the load control signal responsive to a substantially continuously varying
10 amplitude of the AM envelope of the input signal.
- 1 2. The load envelope following amplifier system as in claim 1 wherein the load control
2 signal causes the variable impedance network to vary in discrete steps the various
3 impedances presented to the output of the signal amplifier.
- 1 3. The load envelope following amplifier system as in claim 1 further comprising a
2 control circuit coupled to the envelope mapping circuit for generating a desired average
3 power level signal, wherein the envelope mapping circuit produces the load control signal
4 responsive to the substantially continuously varying amplitude of the AM envelope and
5 the desired average power level signal.
- 1 4. The load envelope following amplifier system as in claim 3 wherein the AM envelope
2 results from a modulation scheme used in a cellular telephone system selected from the
3 group consisting of a time division multiple access (TDMA) system, a code division
4 multiple access (CDMA) system, and a combination TDMA-CDMA system.
- 1 5. The load envelope following amplifier system as in claim 3 wherein the variable
2 impedance network comprises at least one variable element selected from the group
3 consisting of a varactor diode, a voltage variable capacitor, and a plurality of capacitors

alternatively coupled and uncoupled to the output of the signal amplifier, wherein coupling is accomplished using one of micro-electromechanical switches and PIN diodes.

6. The load envelope following amplifier system as in claim 3 further comprising: transmit circuitry coupled to the input of the signal amplifier, the transmit circuitry for amplifying and modulating the input signal before applying the input signal to the signal amplifier; and distortion control circuitry coupled to the envelope mapping circuit and the transmit circuitry, the distortion control circuitry for generating a distortion control signal and coupling the distortion control signal to the transmit circuitry, wherein the distortion control signal causes the transmit circuitry to impress pre-distortion upon the input signal before applying the input signal to the signal amplifier.

7. The load envelope following amplifier system as in claim 3 further comprising: digital processing circuitry for converting user input information to the input signal; transmit circuitry coupling the digital processing circuitry to the input of the signal amplifier, the transmit circuitry for amplifying and modulating the input signal before applying the input signal to the signal amplifier; and distortion control circuitry coupled to any of the digital processing circuitry, the transmit circuitry, and the envelope mapping circuit, the distortion control circuitry for causing any of the digital processing circuitry and the transmit circuitry to introduce pre-distortion upon the input signal before applying the input signal to the signal amplifier.

8. The load envelope following amplifier system as in claim 3 further comprising a signal quality detector coupled to the output of the signal amplifier and coupled to the envelope mapping circuit, the signal quality detector for monitoring a peak-to-average ratio of the amplified version of the input signal and producing an indication of the peak-to-average ratio, wherein the envelope mapping circuit generates the load control signal responsive to the indication of the peak-to-average ratio, thereby causing the variable impedance network to present the various impedances to the output of the signal amplifier that cause the signal amplifier to produce the amplified version of the input signal with a substantially constant, predetermined, peak-to-average ratio.

1 9. The load envelope following amplifier system as in claim 3 further comprising a
2 signal quality detector coupled to the output of the signal amplifier and coupled to the
3 envelope mapping circuit, the signal quality detector for monitoring an adjacent channel
4 power (ACP) of the amplified version of the input signal and for producing an indication
5 of the ACP, wherein the envelope mapping circuit produces the load control signal
6 responsive to the indication of the ACP, thereby causing the variable impedance network
7 to present the various impedances to the output of the signal amplifier to cause the signal
8 amplifier to generate less than a predetermined level of ACP.

1 10. A radio frequency transmitter for transmitting a modulated information signal, the
2 radio frequency transmitter comprising:
3 a control circuit for converting user input information into an input signal;
4 transmitter circuitry coupled to the control circuit for processing the input signal to form
5 a modulated input signal having an amplitude modulation (AM) envelope; and
6 a load envelope following power amplifier (LEF PA) system coupled to the transmitter
7 circuitry, the LEF PA system including,
8 a power amplifier coupled to receive the modulated input signal to produce an
9 amplified signal at an output of the power amplifier,
10 a variable impedance network coupled to receive the amplified signal and a load
11 control signal, the load control signal causing the variable impedance network
12 to present various impedances to the output of the power amplifier, and
13 an envelope mapping circuit coupled to the variable impedance network, the
14 control circuit, and the transmitter circuitry, the envelope mapping circuit to
15 produce the load control signal responsive to amplitude variations of the AM
16 envelope.

1 11. A method of transmitting user input information comprising:
2 converting the user input information into an input signal;
3 modulating a carrier wave signal with the input signal to form a modulated signal having
4 an amplitude modulation (AM) envelope;
5 amplifying the modulated signal having an AM envelope with a power amplifier; and
6 varying a load impedance at an output of the power amplifier substantially in harmony
7 with the AM envelope.

1 12. The method of transmitting user input information as in claim 11 further comprising
2 impressing upon the modulated signal pre-distortion to compensate for distortion
3 impressed upon the modulated signal during any of amplifying the modulated signal and
4 varying the load impedance.

1 13. The method of claim 11 further comprising generating a load control signal
2 responsive to the AM envelope, wherein the step of varying the load impedance is
3 responsive to the load control signal.



INVESTOR IN PEOPLE

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Patents Act 1977
Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.S): H3W WUE

Int Cl (Ed.7): H03F 1/02

Other: ONLINE:WPI,EPODOC,JAPIO

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
A	US 5276912 (MOTOROLA)	1,10,11

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.